

PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Stephen M. Douglass
Assignor: Xilinx, Inc.
Title: "Method of Designing Integrated Circuit Having Both Configurable and Fixed Logic Circuitry"
Serial No.: 10/092,051 File Date: 03-04-02
Examiner: Unknown Art Unit: 2811
Docket No.: X-923 US Conf. No.: 7802

COMMISSIONER FOR PATENTS
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INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the twelve (12) references listed in the attached Substitute for form 1449A/PTO. A copy of each is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

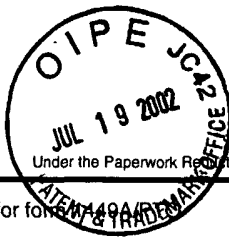
Respectfully submitted,

H. C. Chan
Attorney for Applicant
Reg. No. 35,477

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D. C. 20231, on July 16, 2002.

Pat Slaback
Name

Signature



Substitute for form 1041A/REC

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

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| Sheet | 1 | of | 2 |
|-------|---|----|---|

Complete if Known

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|--------------------------------|---------------------|
| Applicati n / C nf. No. | 10/092,051 / 7802 |
| Filing Date | March 04, 2002 |
| First Named Inventor | Stephen M. Douglass |
| Art Unit | 2811 |
| Examiner Name | Unknown |
| Attorney Docket Number | X-923 US |

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner
Signature

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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PTO/SB/08A (10-01)
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| Substitute for form 1449A-PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary) | | | | Complete if Known | |
| | | | | Application / Conf. No. | 10/092,051 / 7802 |
| | | | | Filing Date | March 04, 2002 |
| | | | | First Named Inventor | Stephen M. Douglass |
| | | | | Art Unit | 2811 |
| | | | | Examiner Name | Unknown |
| Sheet 2 of 2 | | | | Attorney Docket Number | X-923 US |

| OTHER -- NON PATENT LITERATURE DOCUMENTS | | | |
|------------------------------------------|--------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| Examiner Initials * | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | | Cary D. Snyder and Max Baron; "Xilinx's A-to-Z System Platform"; Cahnners Microprocessor; The Insider's Guide to Microprocessor Hardware; Microdesign Resources; February 6, 2001; pp. 1-5. | |
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